

MAY 27 2004

OFFICIAL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Potts

Docket No: TI-27832

Serial No: 10/055,445

Examiner: Gyer, Scott B.

Filed: 1/23/2002

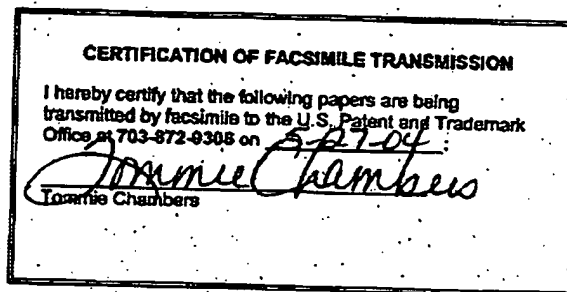
Art Unit: 2829

For: SEMICONDUCTOR WAFER WITH GROUPED INTEGRATED CIRCUIT
DIE HAVING INTER-DIE CONNECTIONS FOR GROUP TESTING

EXTENSION OF TIME

Fee Only

Assistant Commissioner For Patents
Washington, DC 20231



Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petition(s) the Commissioner for an extension of the shortened statutory period for response in the above identified Application.

The fee for this extension is indicated below:

— One Month (\$110)

— Three Months (\$890)

X Two Months (\$390)

— Four Months (\$1,390)

Any further necessary extension of time is hereby requested. Charge any and all fees, or credit any overpayment, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

06/07/2004 AWISE1 00000003 200668 10055445

01 FC:1201 258.00 DA

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633

Respectfully submitted,

W. Daniel Swayze, Jr.
W. Daniel Swayze, Jr.
Attorney for Applicant
Reg. No. 34,476

06/07/2004 AWISE1 00000009-200668 10055445

01 FC:1252 420.00 DA